IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

dinventor(s):

NOV 1 3 2000

Peter Ledel Gammel

Richard Edwin Howard

Omar Daniel Lopez

Wei Zhu

Serial No.:

18-47-1-5

09/651696

Filing Date:

August 30, 2000

Examiner:

Not Assigned

Group Art Unit:

2879

Title:

On-Chip Vacuum Tube Device And Process For Making Device

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D. C. 20231

SIR:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97(b)

In accordance with 37 CFR 1.97(b), the enclosed Information Disclosure Statement, with attached reference(s), is submitted for consideration in the above-identified application.

NO FEE IS REQUIRED

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Lucent Technologies Deposit Account No. 12-2325 as required to correct the error.

Respectfully,

Scott Rittman, Attorney

Reg. No. 39010

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Date: Nov 6, 2000

Dock t Administrator (Room 3C-512)

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Sheet 1 of 1

INFORMATION DISCLOSURE STATEMENT

Case No. Serial No. Applicant: Filing Date: Group: Gammel 18-47-1-57

P.L. Gammel, et al. August 30, 2000

oup: | 2879

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

AR Green, R. et al., "Vacuum Integrated Circuits", IEDM 85, pp. 172-175



110V 14 2007 TC 2300 FAIL ROOM

EXAMINER	DATE CONSIDERED

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.